

Fault modelling and System Simulation of FlowFET MEF Arrays

Objectives

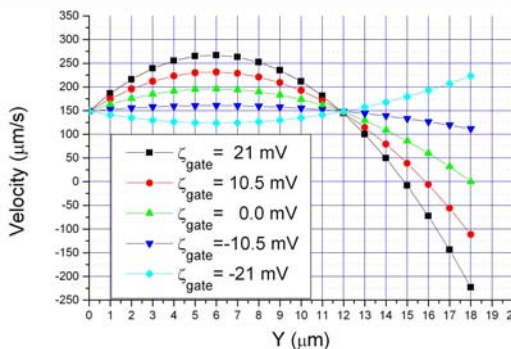
- CDF-ACE+ / CoventorWare fluidic simulations of FlowFET-based modules
- VHDL-AMS modelling of FlowFET-based modules
- Co-simulation of microelectronics and FlowFET modules

Partners involved

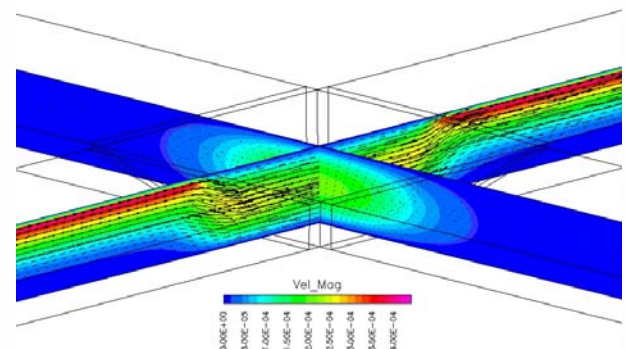
- MESA+ Institute for Nanotechnology, NL
- CCLRC, Daresbury Laboratory, UK
- IMT Bucharest, RO

Summary of results

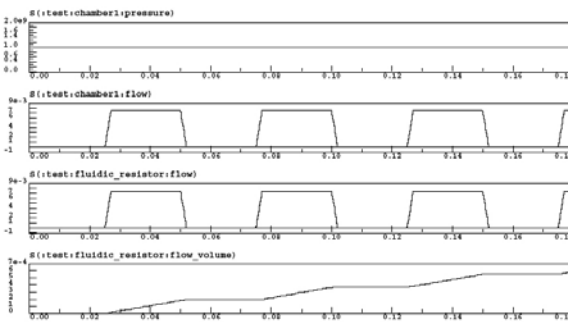
CoventorWare FlowFET



CDF-ACE+ FlowFET crossing



VHDL-AMS FlowFET array



Offer to industry:

- Simulation of FlowFET modules (single & crossings)
- VHDL-AMS modelling of these modules
- Co-simulation of FlowFET-based arrays

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Project status in final phase