

## Workshop on Design for Micro & Nano Manufacture, 30-31 May 2005, DTIP - Agenda

Monday 30 May

- 14:00 – 15:30 **Workshop on “Latest Advances in MEMS Testing”, WP1**  
Session organised by P. Nouet (E-Mail: nouet@lirmm.fr), LIRMM, France  
**Welcome address and WP1 activity overview**, Pascal Nouet, WP1 leader  
**DfT for Biosensors**, Hongyuan Liu, University of Lancaster  
**Bias superposition for MEMS Testing**, Frederick Mailly, LIRMM, Univ. of Montpellier II  
**On-chip testing for mechanical characterization of thin polysilicon films**, Alberto Corigliano, Politecnico di Milano
- 15:50 – 17:20 **Tutorial “Techniques for Failure Analysis of MEMS and MEMS Packages”**  
I. De Wolf (E-Mail: ingrid.dewolf@imec.be), IMEC, Belgium

Tuesday 31 May

- 09:00 – 10:30 **Tutorial “Thermal Characterisation of MEMS Structures and Packages”**  
M. Rencz (E-Mail: rencz@eet.bme.hu), BUTE/MicReD, Hungary
- 10:50 – 12:20 **European Research Programmes in MEMS Packaging / Package Engineering, WP4**  
Session organised by A. Brown (E-Mail: agbrown@QinetiQ.com), QinetiQ, UK  
**Introduction to PATENT-DfMm Package Engineering activities**  
**Guest Presentation: Rapid prototyping and packaging of microsensors for niche products** - Peter Friis, DELTA  
**Investigation of Laser Based Processes for MEMS Assembly and Packaging** – Changhai Wang - Heriot Watt University  
**MOEMS Packaging for Harsh environments** – Jerome Loicq, CSL  
**To develop a methodology and initial tool-set for the assessment of the impact of packaging on MEMS devices** – Alan Brown, QinetiQ
- 14:00 **NEXUS MWG-DMS and NoE PATENT-DfMm Workshop “Design Modelling Simulation (DMS) of Microsystems”**  
This workshop is co-organised by the NEXUS Methodology Working Group "Design Modelling Simulation" (MWG-DMS), formerly USC-CAD, and the EC-funded Network of Excellence "Design for Micro & Nano Manufacture" (PATENT-DfMm). Its objective is to share latest developments and ideas for “Design for Manufacture” methodologies and their potential implementation into algorithms and design tools.
- 14:00 – 15:30 **Session 1: DMS Methodologies towards a "Design for Micro Nano Manufacture Approach"**  
Chaired by P. Salomon (E-Mail: patric.salomon@4m2c.com), 4M2C, Berlin, Germany  
**Overview on the NoE PATENT-DfMm**, Andrew Richardson, Lancaster University, UK  
**NoE PATENT-DfMm: Methodology Development and Interface to Users**, Nicolas Cordero, Tyndall Ireland  
**New Development for mor4ansys: Parametric and Nonlinear Model Reduction**, E. B. Rudnyi, J. G. Korvink, IMTEK, Freiburg, Germany  
**Co-simulation of autonomous microsystems**, Karine Matou, TIMA, Grenoble, France  
**Overview on the PROMENADE project (Process management and design system for microsystem technologies)**, Rainer Brück, Kai Hahn, University of Siegen, Germany
- 15:50 – 17:20 **Session 2 (joint session with the AMICOM workshop): DMS Tools for RF MEMS and DfMm**  
Co-chaired by Larissa Vietzorreck, TU Munich and Patric Salomon, 4M2C, Berlin  
**NoE PATENT-DfMm: Interface to Tools and EDA Providers**, Raluca Muller, IMT Bucharest, Romania  
**Recent DfMm Developments in CoventorWare**, Gerold Schröpfer, Coventor, France  
**SoftMEMS Approach to DfMm**, Ridha Hamza, Mary Ann Maher, SoftMEMS, France/USA  
**EM modeling with MAGMAS 3D**, Yves Schols, K.U.Leuven, Belgium  
**Hybrid electromagnetic/acoustic analysis of TFBAR devices and circuits**, Marco Farina, Università Ancona, Italy  
**Efficient RF Mems design using 3D Finite Difference Time Domain electromagnetic simulations**, W. Simon, IMST GmbH, Germany
- 17:20 – 17:50 Discussion of future needs and wrap-up
- 1 June **Panel on DESIGN FOR MICRO & NANO MANUFACTURE AND PACKAGING within DTIP**  
Moderator: P. Salomon, 4M2C PATRIC SALOMON, Berlin, Germany  
**A. Richardson, U. Lancaster, UK**  
**K. Persson, IMEGO, Sweden**  
**F. Rudolf, Colibrys S.A., Switzerland**  
**J. Deacon, PERA, UK**

Detailed information of the workshop will be updated at: [www.patent-dfmm.org](http://www.patent-dfmm.org).

Most of the presentations are available for (free) download from the PATENT-DfMm website.

The tutorials are available at low cost; please contact George Bell, E-Mail: [George.Bell@slu-institute.ac.uk](mailto:George.Bell@slu-institute.ac.uk)

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