

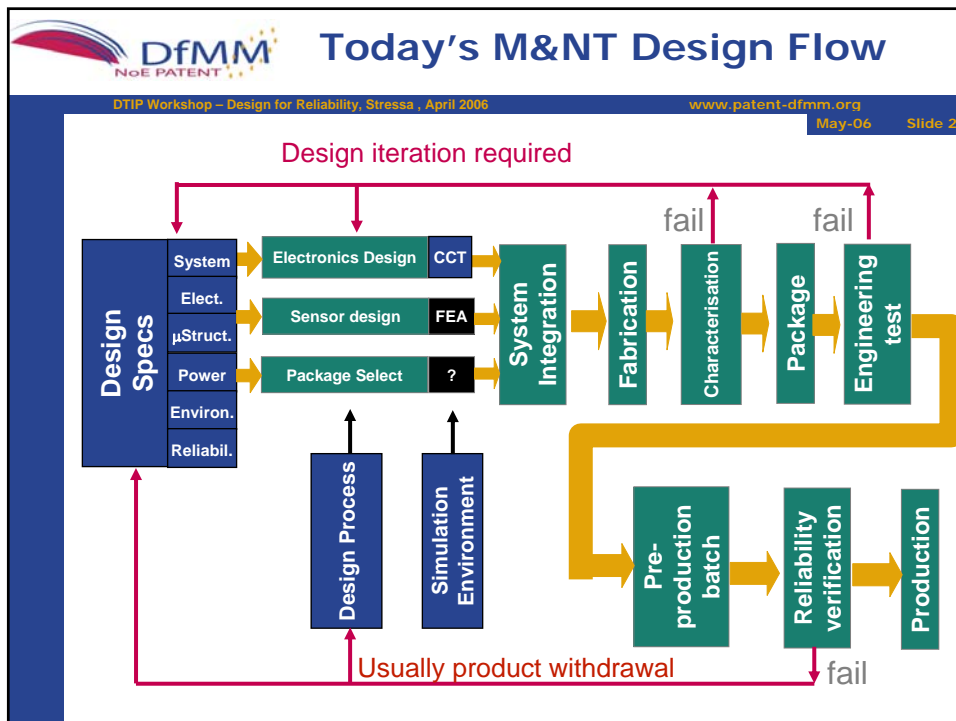


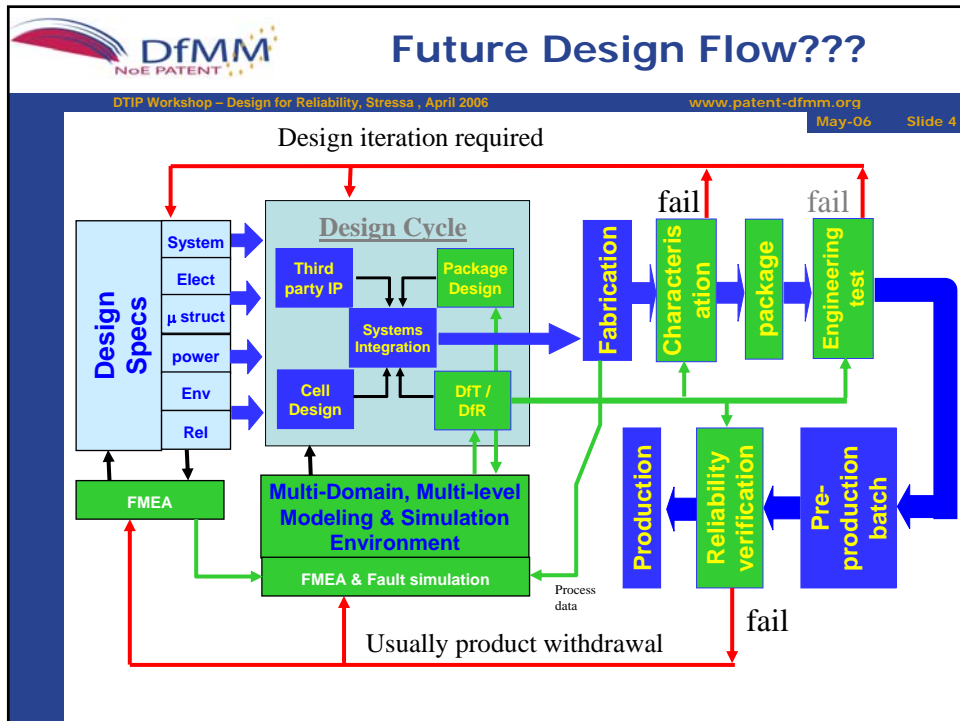
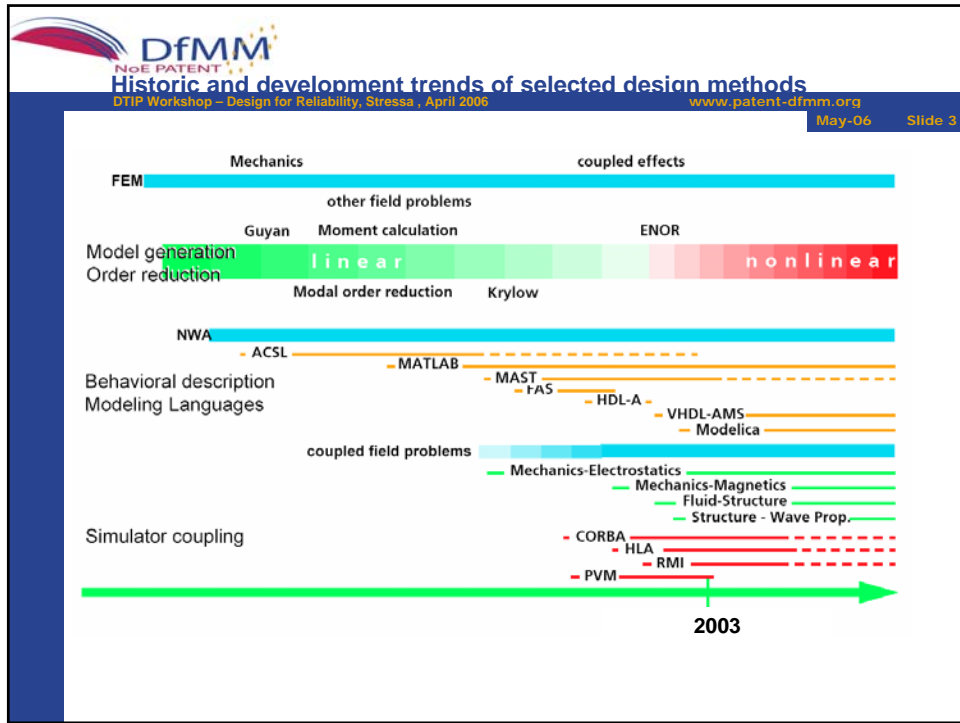
Design for Micro & Nano Manufacture Methodologies – Are they CADABLE?


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May-06

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Discussion

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www.patent-dfmm.org

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- Does the PATENT-DfMM Target methodology need updating?
- How can PATENT-DfMM and other projects contribute to implementing a DfM flow in commercial tools
- What do the EDA vendors need to address
- How close to this vision are mainstream tools